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Facult	y of Electrical E	ngineering			969	
		STUDY MODULE DI	ES	CRIPTION FORM		
Name of the module/subject Co					Coc <b>10</b> 1	e 0324391010325955
Field of study  Electrical Engineering				Profile of study (general academic, practical) (brak)  Yea		Year /Semester 5 / 9
	path/specialty	t Systems in Industry and		Subject offered in:  Polish		Course (compulsory, elective)  obligatory
Cycle of study:			Form of study (full-time,part-time)			
First-cycle studies			part-time			
No. of h	e: - Classe			Project/seminars:	9	No. of credits
Status c	or the course in the study	program (Basic, major, other) (brak)	(	university-wide, from another f	<sub>ieia)</sub> (bra	ak)
Education areas and fields of science and art				ECTS distribution (number and %)		
technical sciences						1 100%
Technical sciences						1 100%
Resp	onsible for subj	ect / lecturer:				
ema tel. (	nž. Arkadiusz Hulewic nil: arkadiusz.hulewicz 61 665 25 46 dział Elektryczny Piotrowo 3A 60-965 Pe	@put.poznan.pl				
Prere	quisites in term	s of knowledge, skills and	d so	ocial competencies:		
1	Knowledge	Basic knowledge in the scope of electrical engineering and electronics  Basic knowledge in the area of electronic analogue circuits				
2	Skills	Ability of the efficient self-education in the area concerned with design and construction od electronic circuits				

# Assumptions and objectives of the course:

- Knowledge of basis of design, assembly and starting of electronic circuits.

coperation in a team

- Knowledge of properties and application possibilities of analog transducers.

# Study outcomes and reference to the educational results for a field of study

Ability of the necessity of broadening of the competencies and the readiness of submitting the

# Knowledge:

Social

competencies

1. Ability to describe application possibilities of current measuring systems - [K\_W14 +++, K\_W18 +]

#### Skills:

3

- 1. Ability to design the measuring systems creatively, using possibilities offered by new technologies, with regard of the limitations of the current level of knowledge and technique - [K\_U03 ++]
- 2. Ability to work independently and as a team in design and construction companies [K\_U21 +]

#### Social competencies:

1. Ability to think and act enterprisingly in the area of the measuring systems to be used in industry - [K\_K01 +, K\_K04 +]

#### Assessment methods of study outcomes

#### Projects:

- continuous estimating with the tests,
- awarding the skill increase,
- evaluation of the knowledge and skills concerning the realization of an individual project, evaluation of the made final project.

# Course description

# Faculty of Electrical Engineering

- Basics of electronic circuits design.
- Principles of electronic circuits design, that have to meet the given assumptions, and their independent assembly.
- Assumptions to be used in assembly and starting of electronic circuits.
- Starting and testing of the designed nd constructed circuit.

#### Basic bibliography:

- 1. U. Tietze, Ch. Schenk, Układy półprzewodnikowe, WNT, Warszawa 2001
- 2. J. Zakrzewski, Czujniki i przetworniki pomiarowe, Wyd. Politechniki Śląskiej, Gliwice 2004
- 3. Z. Kulka, M. Nadachowski, Analogowe układy scalone, WKŁ, Warszawa 1985.

## Additional bibliography:

- 1. A. Guziński, Liniowe elektroniczne układy analogowe, WNT, Warszawa 1994.
- 2. Z. Kulka, A. Libura, M. Nadachowski, Przetworniki analogowo-cyfrowe i cyfrowo-analogowe, WKŁ, Warszawa 1987

# Result of average student's workload

Activity	Time (working hours)
1. Participation in projects classes	9
2. Participation in consulting with the lecturer	3
3. Realization of projects	20

### Student's workload

Source of workload	hours	ECTS					
Total workload	32	1					
Contact hours	14	1					
Practical activities	27	1					